

AMENDMENT TO THE SPECIFICATION

Kindly replace the paragraph on page 8, lines 15-27 with the following paragraph:

Specifically, frequency error estimation according to the present invention is accomplished based on the correlation property in which a PSC is orthogonal to a SSC at every 16 chips as shown in Equation (2).

$$\sum_{i=16I}^{16(I+J)-1} C_p(i) C_s^n(i) = 0 \quad \dots(2)$$

where n indicates the index of a SSC and is one of 1, 2, ... 16, I is zero or a positive number larger than 0, J is 1 or a positive number larger than 1, and $(I+J) \leq 16$ ~~$(I+J) \neq 16$~~ . In other words, the orthogonal property between a PSC and a SSC is maintained even though partial period correlation is performed on only the PSC and SSC having a chip length which is integral times of 16, instead of performing entire period correlation on the 256-chip PSC and SSC.